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OIPE IAP57 SEP 23 2005 PATENT & TRADEMARK OFFICE

PTO/SB/21 (08-00)

TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number	09/321,715
Filing Date	May 28, 1999
First Named Inventor	Yasuhiko TAKEMURA et al.
Group Art Unit	2822
Examiner Name	Mary A. Wilczewski
Attorney Docket Number	0756-1974

ENCLOSURES *(check all that apply)*

- ☐ Fee Transmittal Form
 - ☐ Fee Attached
- ☐ Amendment / Reply
 - ☐ After Final
 - ☐ Affidavits/declaration(s)
- ☐ Extension of Time Request
- ☐ Express Abandonment Request
- ☐ Information Disclosure Statement
- ☐ Certified Copy of Priority Document(s)
- ☐ Response to Missing Parts/ Incomplete Application
 - ☐ Response to Missing Parts under 37 CFR 1.52 or 1.53
- ☐ Assignment Papers *(for an Application)*
- ☐ Drawing(s)
- ☐ Declaration and Power of Attorney
- ☐ Licensing-related Papers
- ☐ Petition
- ☐ Petition to Convert to a Provisional Application
- ☐ Power of Attorney, Revocation Change of Correspondence Address
- ☐ Terminal Disclaimer
- ☐ Request for Refund
- ☐ CD, Number of CD(s) _____
- ☐ After Allowance Communication to Group
- ☐ Appeal Communication to Board of Appeals and Interferences
- ☐ Appeal Communication to Group *(Appeal Notice, Brief, Reply Brief)*
- ☐ Proprietary Information
- ☐ Status Letter
- ☒ Other Enclosures
 1. Request for Certificate of Correction/Form 1050
 2.
 3.
 4.
 5.
 6.

Remarks

☒ The Commissioner is hereby authorized to charge any additional fees required or credit any overpayments to Deposit Account No. 50-2280 for the above identified docket number.

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Eric J. Robinson, Reg. No. 38,285 Robinson Intellectual Property Law Office, P.C. PMB 955 21010 Southbank Street Potomac Falls, VA 20165
Signature	
Date	9-22-05

Certificate SEP 28 2005 of Correction

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

Type or printed name	Adele M Stamper		
Signature	Adele M Stamper	Date	9.22.05

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.

SEP 28 2005

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Date	9-22-05

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PATENT & TRADEMARK OFFICE

Attorney Docket No. 0756-1974

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:	)	Group Art Unit: 2822
Yasuhiko TAKEMURA et al.	)	Examiner: M. Wilczewski
Patent No.: 6,841,432 B1	)	CERTIFICATE OF MAILING
Issued: January 11, 2005	)	I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on 9.22.05.
Serial No. 09/321,715	)	
Filed: May 28, 1999	)	
For: SEMICONDUCTOR DEVICE AND PROCESS FOR FABRICATING THE SAME	)	Adele M Stamp

REQUEST FOR CERTIFICATE OF CORRECTION UNDER 37 CFR § 1.322 FOR CORRECTION OF OFFICE MISTAKE

ATTN: Certificate of Correction Branch
Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

As provided in detail in the attached, the patentee respectfully requests that a Certificate of Correction be granted in the above-identified patent to correct minor clerical, typographical or grammatical errors a mistake in a patent, incurred through the fault of the Office.

Under 37 CFR 1.322, "The Commissioner may issue a certificate of correction pursuant to 35 U.S.C. 254 to correct a mistake in a patent, incurred through the fault of the Office, which mistake is clearly disclosed in the records of the Office at the request of the patentee or the patentee's assignee."

The Patentee furthermore requests Expedited Issuance of this Certificate of Correction in accordance with MPEP § 1480.01. Specifically, this section provides that:

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> In an effort to reduce the overall time required in processing and granting Certificate of Correction requests, the Office will expedite processing and granting of patentee requests where such requests are accompanied by evidence to show that the error is attributable solely to the Office Where the correction requested was incurred through the fault of the Office, and the matter is clearly disclosed in the records of the Office, and is accompanied by documentation that unequivocally supports the patentee's assertion(s), a Certificate of Correction will be expeditiously issued.

The patentee filed a *Rule 312 Amendment* on June 29, 2004 (received by OIPE July 1, 2004), requesting correction of claims 49 and 56, which correspond with claims 57 and 66, respectively, of the resulting U.S. Patent No. 6,841,432. Specifically, the *Amendment* requested addition of ", for electrically isolating said plurality of thin film transistors from one another," in the oxidizing step of claims 49 and 56.

In a *Response to Rule 312 Communication* mailed November 17, 2004, the *Rule 312 Amendment* was disapproved, and the *Response* asserted that "the proposed amendment is not supported by the originally-filed specification or figures" (Paper No. 11122004).

However, the patentee filed a *Request for Reconsideration of Rule 312 Amendment* on December 20, 2004 (received by OIPE December 21, 2004), presenting detailed arguments as to why the *Rule 312 Amendment* was supported by the originally-filed specification or figures. The *Request for Reconsideration* was not acted upon by the Office. Specifically, the *Request for Reconsideration* included the following arguments regarding support for the feature of electrical isolation in claims 49 and 56 (issued as claims 57 and 66; also, please note, claim 63 during prosecution issued as claim 74):

> Initially, it is noted that allowed claim 63 in the present application already recites this feature. Presumably, claim 63 does not suffer from a lack of support. Therefore, it should follow that claims 49 and 56, as amended, do not suffer from a lack of support.
>
> In any event, electrical isolation of thin film transistors is discussed throughout the specification and drawings, for example, at page 1, line 14+; page 4, line 1+; page 11, line 2+; page 16, line 2+; page 25, line 8+;

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Figure 6(A); and in the abstract. In particular, it is noted that the Summary of the Invention beginning at page 4, line 6, teaches the following: "The present invention comprises electrically isolating the elements (e.g. the transistors) from each other not by cutting the silicon film but by selectively and thermally oxidizing the silicon film in the temperature range of from 500 to 650 °C. The silicon film to be oxidized may be either an amorphous film or a crystalline film." Therefore, the originally filed specification and drawings fully support the feature of oxidizing a crystallized semiconductor film for electrically isolating a plurality of thin film transistors from one another. Reconsideration of the disapproval and entry of the *Rule 312 Amendment* are respectfully requested.

An *Issue Notification* was sent in late December 2004, and the patent issued January 11, 2005.

The patentee respectfully submits that the *Request for Reconsideration* clearly identifies the support for the feature of electrical isolation. Because the *Request for Reconsideration* was not acted upon by the Office, the patentee's arguments regarding support for the *Rule 312 Amendment* were not considered, the *Rule 312 Amendment* was not entered, and errors resulted in claims 57 and 66 of the issued patent.

The patentee respectfully submits that these errors are the result of an Office mistake in the handling of the *Request for Reconsideration*. Therefore, the patentee respectfully requests entry of the *Request for Reconsideration* and correction of the claims in accordance with the same. Specifically, in claim 57, line 10, and in claim 66, line 10, after "and" the patentee requests addition of --, for electrically isolating said plurality of thin film transistors from one another,--.

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As the errors were incurred through the fault of the Office, a fee is not believed to be necessary. Should it be determined that a fee is necessary, any deficiencies or overages in any fees due in connection with this patent and the requested actions should be applied to Deposit Account No. 50-2280.

Respectfully submitted,

Eric J. Robinson
Reg. No. 38,285

Robinson Intellectual Property Law Office, P.C.
PMB 955
21010 Southbank Street
Potomac Falls, Virginia 20165
(571) 434-6789

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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 6,841,432 B1

DATED : January 11, 2005

INVENTOR(S) : Yasuhiko TAKEMURA et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 57, line 10, and claim 66, line 10, after "and" please insert --, for electrically isolating said plurality of thin film transistors from one another,--.

MAILING ADDRESS OF SENDER:

PATENT NO. 6,841,432 B1

Eric J. Robinson
Robinson Intellectual Property Law Office
PMB 955
21010 Southbank Street
Potomac Falls, VA 20165

No. of additional copies

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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